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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<i>Complete if Known</i>	
		Application Number	
		Filing Date	
		First Named Inventor	NIKOLAOS BELLAS
		Group Art Unit	
Examiner Name			
Sheet	1	of	2
		Attorney Docket Number	CML00059D

U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code <sup>2</sup>	Kind (if known)			
B	1	5,099,325		ARTIERI ET AL.	03/24/92	
B	2	5,473,378		TAMITANI	12/05/95	
B	3	5,512,962		HOMMA	04/30/96	
B	4	5,594,813		FANDRIANTO ET AL.	01/14/97	
B	5	5,608,656		PURCELL ET AL.	03/04/97	
B	6	5,630,033		PURCELL ET AL.	05/13/97	
B	7	5,652,625		CHEN ET AL.	07/29/97	
B	8	5,838,392		CHEN ET AL.	11/17/98	
B	9	5,901,248		FANDRIANTO ET AL.	05/04/99	
B	10	5,905,542		LINZER	05/18/99	
B	11	5,953,458		PIRSON ET AL.	09/14/99	
B	12	5,987,178		ANEKSO ET AL.	11/16/99	
B	13	6,104,836		BUCKLEY ET AL.	08/15/00	
B	14	6,122,442		PURCELL ET AL.	09/19/00	
B	15	6,160,850		CHEN ET AL.	12/12/00	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>8</sup>
		Office <sup>3</sup> Code <sup>2</sup>	Number <sup>4</sup>	Kind (if known)				

Examiner Signature		Date Considered	11/8/04
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
B	16	MARK QUEROL, "STi 3220 Motion Estimation Processor Codec", SGS Thomson Microelectronics – Application Note, 1994, pp. 1-33.	
B	17	"Summary of A436™ Parallel Video DSP Chips", Oxford Micro Devices – Rev. June 26, 2001, pp. 1-8.	
B	18	"DSP Selection Guide", Texas Instruments, 5/2001, pp. 1-47,	
B	19	K-M YANG, et al. "A family of VLSI designs for the motion compensation block-matching algorithm", <i>IEEE Transactions on Circuits and Systems</i> , 36(10):1317-1325, October 1989	
B	20	P.A RUETZ, et al. "A high-performance full motion video compression chip set", <i>IEEE Transactions on Circuits and Systems for Video Technology</i> , 2(2):111-121, June 1992	
B	21	THOMAS KOMAREK and PETER PIRSCH, "Array architectures for block-matching algorithms", <i>IEEE Transactions on Circuits and Systems</i> , vol. 36, No. 10, Oct. 1989, pp.1301-1308.	

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